			8		
Form PTO-1449 (modified)		Atty. Docket No.	Serial No.		
		2000.045200/TT3253/JCJ	09/731,579		
P List of Patents and Publications for Applicant's		Applicant(s)			
/0		Terrence J. Riley, William	n Jarrett Campbell		
NOV 0 1 200ENFORMATION DISCLOSURE S	STATEMENT				
NOV BE		Filing Date:	Group:		
(Use several sheets if necess	December 6, 2000				
(Use several sheets if necess:  TRADENIU.S. Patent Documents  See Page 1	Foreign	Patent Documents	Other Art		
See Page 1		See Page 1	See Page 1		

## **U.S. Patent Documents**

Exam. Init.	Ref. Des.	Document Number	Date	Name	Clas s	Sub Class	Filing Date of App.
Ron	A1	5,848,842	12/15/1998	PEUSE ET AL.	374	1	05/20/1986
CRUR	A2	5,991,699	11/23/1999	KULKARNI ET AL.	702	83	04/02/1997
Clar	A3	6,028,994	02/22/2000	PENG ET AL.	395	500.36	05/06/1998
CROR	A4	6,055,460	04/25/2000	SHOPBELL	700	121	08/06/1997

## **Foreign Patent Documents**

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
	Bl						RECEIVED
	B2						I ILOLI V
	В3						NOA 0 6 5005

## Other Art (Including Author, Title, Date Pertinent Pageshnelogy Center 2100

Exam. Init.	Ref. Des.	Citation
CROK	C1	International Search Report dated October 1, 2002 for International application no. PCT/US01/44112 Filed November 6, 2001
CREM	C2	POTTS, D.C.: "Manufacturing for Design: Putting Process Control in the Language of the Designer," ADVANCED SEMICONDUCTOR MANUFACTURING CONFERENCE AND WORKSHOP, 1998. 1998 IEEE/SEMI BOSTON, MA, USA 23-25, SEPT. 1998, NEW YORK, NY, USA, IEEE, US PAGES 187-189 XP010314134 ISBN: 0-7803-4380-8
	C3	

EXAMINER: La College Date Considered: 4/18/04

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